

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

re Patent Application of

Yuri Mirgorodski et al.

Application No.: 10/664,758

Filed: September 17, 2003

For: AN EFFICIENT METHOD OF PMOS

STACKED-GATE MEMORY CELL PROGRAMMING UTILIZING

FEEDBACK CONTROL OF SUBSTRATE CURRENT Group Art Unit: 2818

Examiner: Unknown

PRELIMINARY AMENDMENT

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Mail Stop MISSING PARTS Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 **CERTIFICATE OF MAILING**

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope, addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on January 14, 2004

STALLMAN & PÓLLOCK LLP

Dated: 01/1/04

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Janet Chan

Sir:

Please amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of claims that begins on page 2 of this paper.

Remarks begins on page 4 of this paper.

Atty Docket No.: NSC1-M3600 (PO5682)